

FIG.2 101 CONTROLLER 111 102 **OPERATION PART** CPU 113 108 103 104 106 2nd PCI 109 **ASIC** NB MEM-P **AGP ENGINE** 105 MEM-C SB 110 112 107

FIG.3

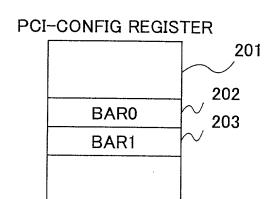


FIG.4

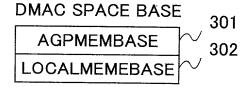


FIG.5

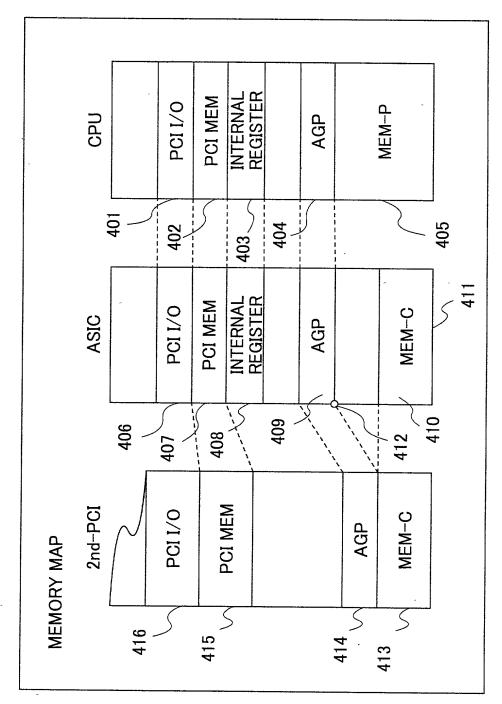
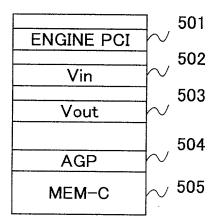


FIG.6

2nd-PCI SPACE



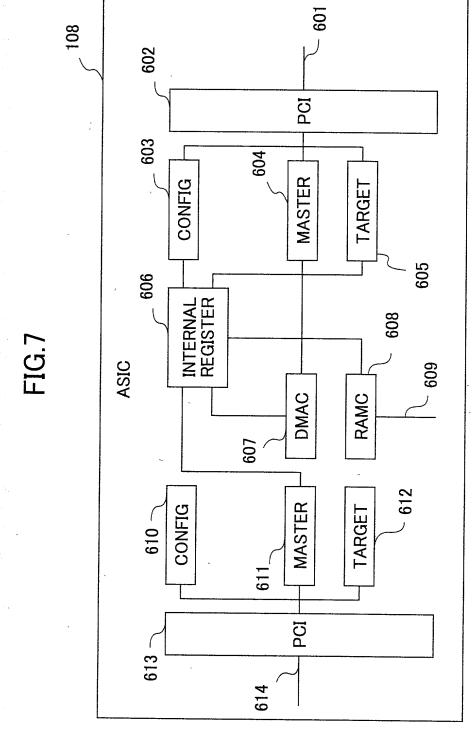


FIG.8

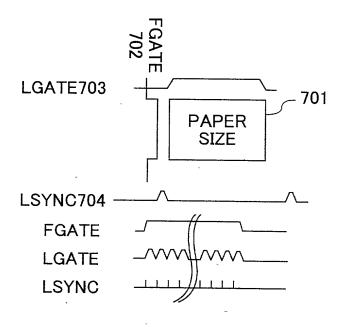


FIG.9

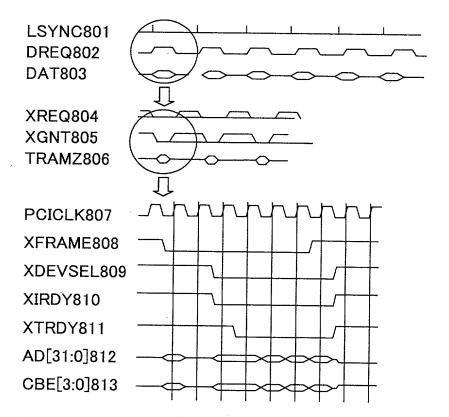


FIG.10

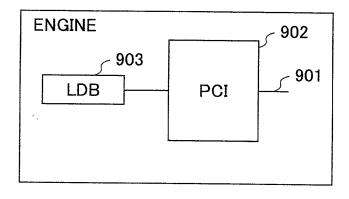
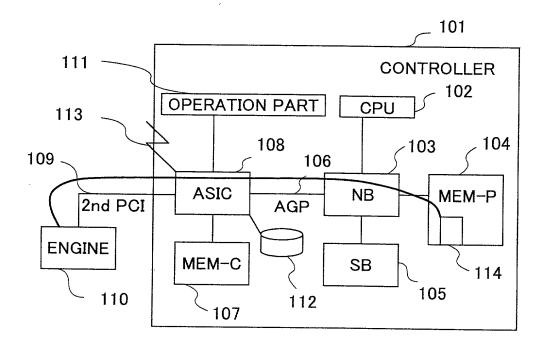


FIG.11



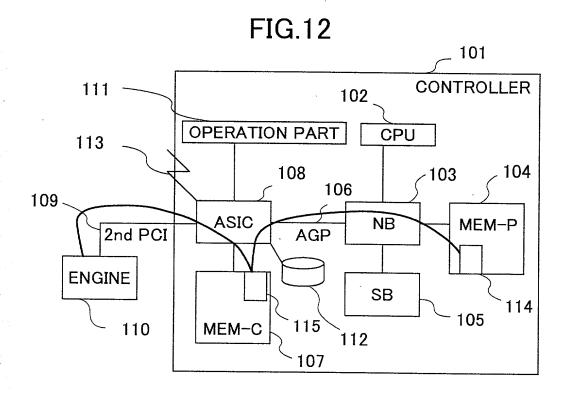
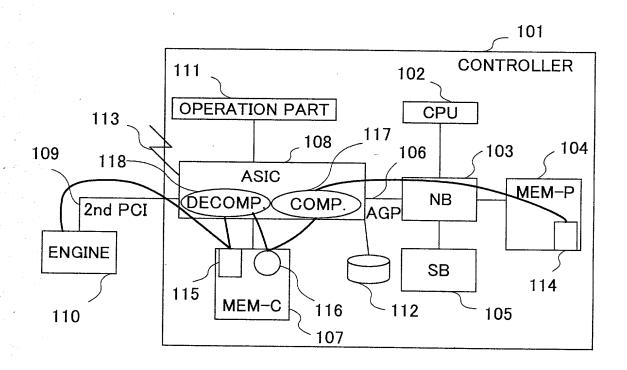


FIG.13



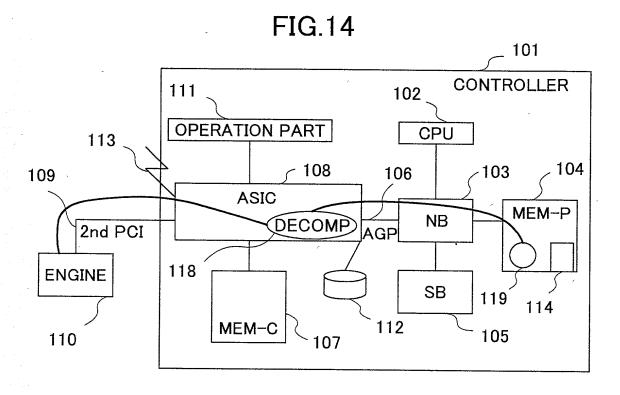
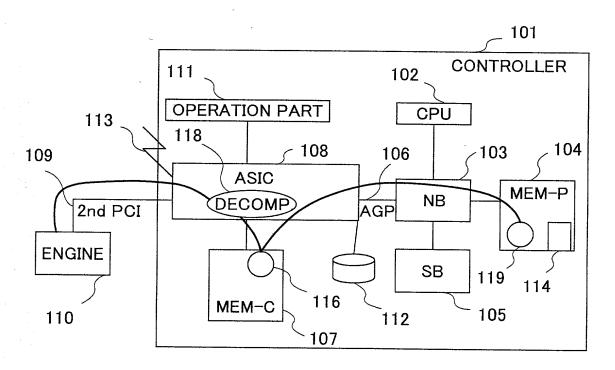


FIG.15



THE RESIDENCE OF THE PARTY OF T

FIG.16

